File   2:INSPEC 1969-2003/Jun W5 (c) 2003 Institution of Electrical Engineers
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File   15:ABI/Inform(R) 1971-2003/Jul 12 (c) 2003 ProQuest Info&Learning
File   16:Gale Group PROMT(R) 1990-2003/Jul 14 (c) 2003 The Gale Group
File   20:Dialog Global Reporter 1997-2003/Jul 12 (c) 2003 The Dialog Corp.
File   35:Dissertation Abs Online 1861-2003/Jun (c) 2003 ProQuest Info&Learning
File   65:Inside Conferences 1993-2003/Jul W1 (c) 2003 BLDSC all rts. reserv.
File   99:Wilson Appl. Sci & Tech Abs 1983-2003/May (c) 2003 The HW Wilson Co.
File   148:Gale Group Trade & Industry DB 1976-2003/Jul 14 (c)2003 The Gale Group
File   160:Gale Group PROMT(R) 1972-1989 (c) 1999 The Gale Group
File   233:Internet & Personal Comp. Abs. 1981-2003/May (c) 2003 Info. Today Inc.
File   256:SoftBase:Reviews,Companies&Prods. 82-2003/Jun (c)2003 Info.Sources Inc
File   275:Gale Group Computer DB(TM) 1983-2003/Jul 14 (c) 2003 The Gale Group
File   347:JAPIO Oct 1976-2003/Mar(Updated 030703) (c) 2003 JPO & JAPIO
File   348:EUROPEAN PATENTS 1978-2003/Jul W01 (c) 2003 European Patent Office
File   349:PCT   FULLTEXT   1979-2002/UB=20030710,UT=20030703   (c)   2003
WIPO/Univentio
File   474:New York Times Abs 1969-2003/Jul 11 (c) 2003 The New York Times
File   475:Wall Street Journal Abs 1973-2003/Jul 11 (c) 2003 The New York Times
File   476:Financial Times Fulltext 1982-2003/Jul 12 (c) 2003 Financial Times Ltd
File   583:Gale Group Globalbase(TM) 1986-2002/Dec 13 (c) 2002 The Gale Group
File   610:Business Wire 1999-2003/Jul 12 (c) 2003 Business Wire.
File   613:PR Newswire 1999-2003/Jul 12 (c) 2003 PR Newswire Association Inc
File   621:Gale Group New Prod.Annou.(R) 1985-2003/Jul 14 (c) 2003 The Gale Group
File   624:McGraw-Hill Publications 1985-2003/Jul 11 (c) 2003 McGraw-Hill Co. Inc
File   634:San Jose Mercury Jun 1985-2003/Jul 11 (c) 2003 San Jose Mercury News
File   636:Gale Group Newsletter DB(TM) 1987-2003/Jul 14 (c) 2003 The Gale Group
File   810:Business Wire 1986-1999/Feb 28 (c) 1999 Business Wire
File   813:PR Newswire 1987-1999/Apr 30 (c) 1999 PR Newswire Association Inc

Set   Items   Description
S1   205968   (CRYPTO????? OR ENCRYPT????? OR COD??? OR
AUTHENTICAT????) (5N) (PROCESS?????? OR MICROPROCESS???? OR CPU OR
COMPUTER)
S2   5872   S1 (5N) (ASSIGN????? OR SELECT????? OR CHOOS??? OR CHOS???)
S3   276   S2 (20N) (FRANK??? OR MAIL??? OR SHIPMENT OR POST????? OR
SHIPPING OR SHIP OR INDICIA OR INDICIUM OR INPRINT OR IMPRINT OR
IMPRESSION OR INPRESSION OR DISPENS???? OR TRANSACTION)
S4   5215   S1 (5N) (PARALLEL OR PIPELINE OR MULTIPLE OR MULTI OR
PLURAL)
S5   20   S3 AND S4
S6   19   RD S5 (unique items) [Scanned ti,pd,kwic all]

6/9/1   (Item 1 from file: 2)
DIALOG(R)File   2:INSPEC (c) 2003 Institution of Electrical Engineers. All rts. reserv.

5148078   INSPEC Abstract Number: C9602-6150C-009
      Title: Giving linkers their due
      Author(s): Szewerenko, L.; Mordoh, A.
      Journal: Electronic Design   vol.43,   no.24 p.131-2, 134-5, 139-40
      Publisher: Penton Publishing,
      Publication Date: 20 Nov. 1995
      Country of Publication: USA
      CODEN: ELODAW
      ISSN: 0013-4872
      SICI: 0013-4872(19951120)43:24L.131:GLT;1-Z
      Material Identity Number: E140-95025
      U.S. Copyright Clearance Center Code: 0013-4872/95/$2.00+1.00
      Language: English
      Document Type: Journal Paper (JP)
      Treatment: Practical (P)

      Abstract: Linkers are often maligned by software developers. Given today`s hardware architectures, linkers sometimes appear to be the unnecessary remnants of an earlier archaic technology. Especially in the world of "self-hosted" compilers, linkers are often treated as the toolset`s appendix. Yet we would argue that a wide range of *post*-compilation issues are best addressed by a sophisticated linker. Address computations, elimination of dead *code*, *assignment* of tasks to *processors*, and integration of *multiple* language environments are examples of work that should not be foisted on the compiler. Indeed, in the world of cross-development for real-time embedded systems, linkers are crucial to developing practical high-performance applications. A linker is the key to working with many issues central to embedded systems and digital signal processors (DSPs): use of RAM vs. ROM; separate code and data buses; shared vs. local memory; memory overlays; working with fixed hardware addresses; and effectively using both fast and slow memory. (0 Refs)
      Subfile: C
      Descriptors: program compilers; programming environments; real-time
systems; shared memory systems
      Identifiers: linkers; software development; address computations;
post-compilation issues; multiple language environments; real-time embedded
systems; digital signal processors; local memory; shared memory; memory
overlays
      Class Codes: C6150C (Compilers, interpreters and other processors); C6115
(Programming support)

US-PAT-NO: 4632252
DOCUMENT-IDENTIFIER: US 4632252 A
TITLE: Mail sorting system with coding devices
DATE-ISSUED: December 30, 1986
INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Haruki; Kazuhito	Kanagawa	N/A	N/A	JP
Iwamoto; Masanori	Tokyo	N/A	N/A	JP
Tamada; Masuo	Kanagawa	N/A	N/A	JP

US-CL-CURRENT: 209/546, 209/584 , 209/900 , 382/102 , 700/224

ABSTRACT: A mail sorting system is provided which includes a plurality of mail sorters and coding devices. These mail sorters and coding devices are selectively coupled together by a distributor so that any coding device can be coupled to any mail sorter. When a mail sorter cannot identify the zip code of a particular piece of mail, the unrecognized address image is transferred to one of the coding devices so that the correct address can be input by an operator. An assigning controller is connected to the distributor for determining which coding device should receive the unrecognized address image in accordance with the current mail processing capability of the mail sorters and coding devices in order to maximize the mail handling capability of the mail sorting system.
4 Claims,   16 Drawing figures
Exemplary Claim Number: 1
Number of Drawing Sheets: 7

---------- KWIC ---------
Detailed Description Text - DETX (20): FIG. 12 shows another embodiment of the control table for assigning coding devices. In this embodiment, the control table is a two-dimensional array in which the amount of rejected data for the n-th sorter is coded by the m-th coding device. This control table can be easily generated and updated by distributor 2. From this control table, assigning controller 4 can easily determine which mail sorter or sorters generate excessive amounts of rejected data or which coding device has very high performance and so on. Thus, for example, when the amount of rejected data from a certain mail sorter in a given period reaches a very high value, assigning controller 4 can assign a high performance coding device to this mail sorter to process excess rejected images before mail reaches sorting section 12.
Claims Text - CLTX (4): control means coupled between said sorting devices and said coding devices for distributing the rejected data generated by said recognition means to selected coding devices and for transferring the destination code entered by said input means to said sorting means from which the rejected data originated, said control means including control table means for storing and updating a control table containing control information related to the frequency of rejected data in each of said sorting devices and the processing capability of said coding devices, said control means being responsive to the control information in the control table for selectively processing the rejected data to maximize the mail handling capability of said plurality of mail sorters and assigning one of said coding devices to display the rejected data in accordance with the processing capability of said coding devices.

1

US-PAT-NO: 6073125
DOCUMENT-IDENTIFIER: US 6073125 A
TITLE: Token key distribution system controlled acceptance mail payment and evidencing system
DATE-ISSUED: June 6, 2000
INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Cordery; Robert A.	Danbury	CT	N/A	N/A
Pintsov; Leon A.	West Hartford	CT	N/A	N/A

US-CL-CURRENT: 705/60, 705/1 , 705/400 , 705/401 , 705/402 , 705/403 , 705/408 , 705/51 , 705/63 , 705/64

ABSTRACT: A method for controlled acceptance mail accounting and evidencing, includes creating a mail batch including at least one mail piece having an encrypted indicia printed thereon. The encrypted indicia is generated by the use of a key K.sub.m. A statement of mailing file is created containing data related to the mail batch. The statement of mailing file including the key K.sub.m is encrypted with a public key of an authentication unit of a carrier distribution system. The public key has an associated private key. The mail batch and the statement of mailing file is submitted to the carrier distribution system and the mail batch and the mail documentation file is processed using the private key of the authentication unit of the carrier distribution system to decrypt the encrypted information and recover the K.sub.m. The key K.sub.m is then used in an authentication procedure related to the mail batch. The statement of mailing file may be sent in tangible form or in electronic form to the carrier distribution system. A plurality of public keys of a plurality of authentication units may be available for encryption of the statement of mailing file including the key K.sub.m. The encryption process may be implemented separately with different public keys of different authentication units when it is not yet determined which authentication unit will perform the processing. This provides greater flexibility in the selection of the authentication unit to process the mail and achieve systems efficiency.
21 Claims,   5 Drawing figures
Exemplary Claim Number: 1
Number of Drawing Sheets: 3

---------- KWIC ---------

Abstract Text - ABTX (1): A method for controlled acceptance mail accounting and evidencing, includes creating a mail batch including at least one mail piece having an encrypted indicia printed thereon. The encrypted indicia is generated by the use of a key K.sub.m. A statement of mailing file is created containing data related to the mail batch. The statement of mailing file including the key K.sub.m is encrypted with a public key of an authentication unit of a carrier distribution system. The public key has an associated private key. The mail batch and the statement of mailing file is submitted to the carrier distribution system and the mail batch and the mail documentation file is processed using the private key of the authentication unit of the carrier distribution system to decrypt the encrypted information and recover the K.sub.m. The key K.sub.m is then used in an authentication procedure related to the mail batch. The statement of mailing file may be sent

in tangible form or in electronic form to the carrier distribution system. A plurality of public keys of a plurality of authentication units may be available for encryption of the statement of mailing file including the key K_m. The encryption process may be implemented separately with different public keys of different authentication units when it is not yet determined which authentication unit will perform the processing. This provides greater flexibility in the selection of the authentication unit to process the mail and achieve systems efficiency.

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	153766	(cryptology or crypto or encrypt or encrypting or encrypted or encryption or coding or code or authentication or authenticated or authenticating or authenticate) near5 (processer or processor or processing or process or microprocess$4 or cpu or computer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:09
2	BRS	L2	6444	1 near5 (assign or assigning or assignment or selecting or selection or select or choosing or chose or choose or chosen)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:09
3	BRS	L3	116	2 near20 (franking or mailing or shipment or postage or shipping or frank or mail or ship or postmark or indicia or indicium or inprint or imprint or impression or inpression or dispense or transaction or dispensing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:09
4	BRS	L4	5374	1 near5 (parallel or pipeline or multiple or multi or plural)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:10
5	BRS	L5	1	3 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:11
6	BRS	L6	116	3 or 5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:19

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	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L8	48	("0129781" or "4780828" or "5805701").pn. or ((@pd<=19710101 not @pd<=19470101) and (101/71 or 283/71 or 705/7 or 705/8 or 705/60 or 705/61 or 705/62 or 705/401 or 705/408 or 705/410).ccls.) ← *Scanned* *Ti all*	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:58
8	BRS	L9	18	simcik.in. and crowe.in. *Scanned Ti,Ab all*	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB; USOCR	2003/07/12 11:58

	Document ID	Issue Date	Inventor	Current OR	Current XRef	Pages
1	JP 11027311 A	19990129	KANEHARA, KATSUMI		705/1; 705/400; 705/401; 705/402;	10
2	US 6073125 A	20000606	Cordery, Robert A. et al.	705/60	705/403; 705/408; 705/51; 705/63; 705/64	8
3	US 4632252 A	19861230	Haruki, Kazuhito et al.	209/546	209/584; 209/900; 382/102; 700/224	12

L6 results